Graph analytics on RISC-V GPU Where are the bottlenecks? Nimish Shah and Marian Verhelst nimish.shah@esat.kuleuven.be

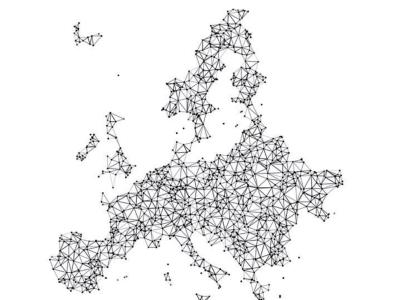


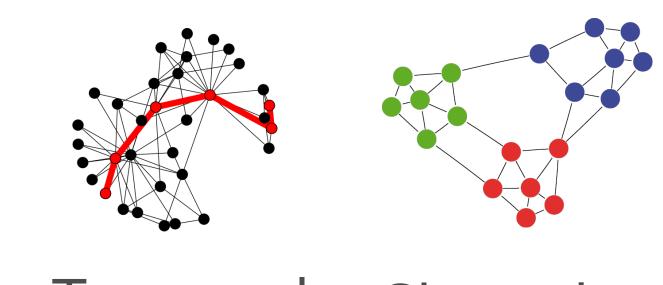


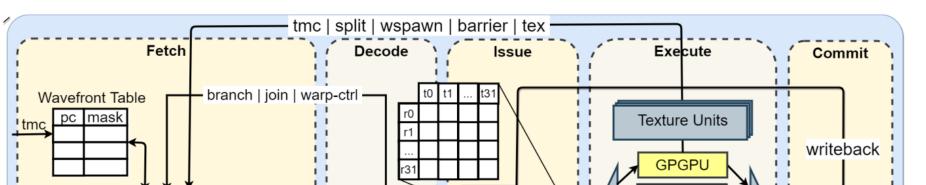
Aim

Benchmarking and analyzing Graph Analytics throughput on a RISC-V GPU

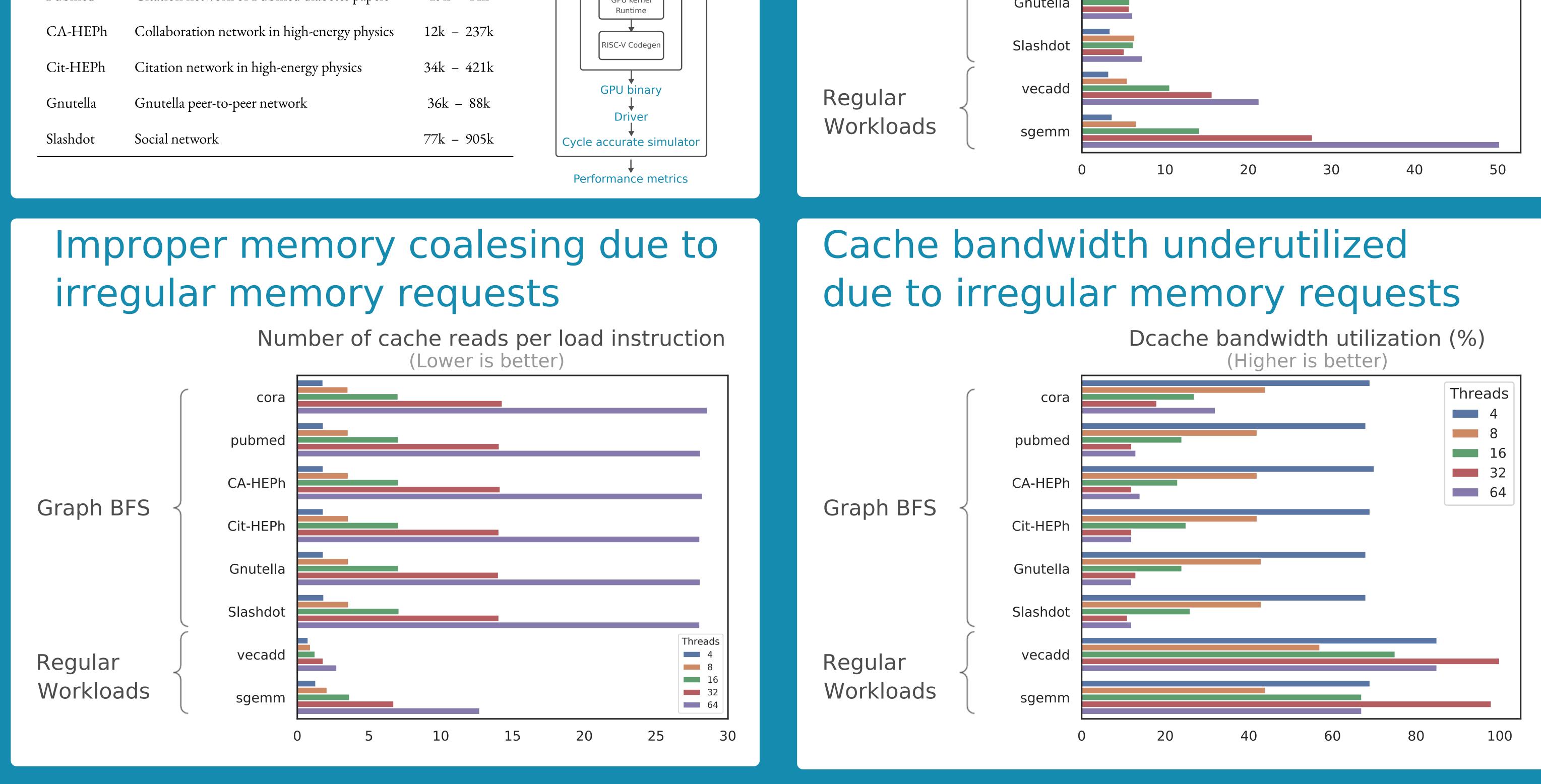








Social networks Road networks	Traversal Image: Constrained state st	ClusteringImage:	PDOM Wavefront Cache
Real-world graphs		lytics	Vortex- a RISC-V GPU [1]
Benchmarking flow	Graph BFS in OpenCL Vortex flow [1]	Graph BF	S achieves low throughput
Benchmarking flow Analytics kernel used: Breadth-first search (BFS) Target graphs		Graph B	Sachieves low throughput
Analytics kernel used: Breadth-first search (BFS)	Vortex flow [1] POCL compiler Clang/LLVM		cora Instructions per cycle (IPC) (Higher is better)
Analytics kernel used: Breadth-first search (BFS) Target graphs	Vortex flow [1] POCL compiler Clang/LLVM LLVM-IR Workgroup	Graph BFS	cora pubmed Instructions per cycle (IPC) (Higher is better) Threads 4 8 16 32



Conclusions

- Graph analytics throughput does not scale with more parallel hardware threads in GPU

- GPU memory hierarchy remains underutilized due to irregular memory requests
- Future work: Upgrade the memory heirarchy to minimize the impact of irregular requests

[1] Tine et al., "Vortex: Extending the RISC-V ISA for GPGPU and 3D-Graphics", MICRO, 2021.